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Organized by
Department of Computer Science & Engineering
GIET University, Gunupur-765022, India



Special Session Title: High Performance Reliable Computing Systems

Organized by:

Name: **Dr. Nibedita Adhikari**

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Theme:

This special session aims to serve as a platform for exchange of proficiencies and novice ideas in computational science. It focuses essentially on tools, algorithms, strategies, challenges and usage of high-performance computing systems. Also Internet of Things is playing a vital role in everyday scenario now-a-days. It is becoming popular as it enables the user's device to be linked with Internet and is also connected to the sensors, actuators as well as accelerators. These computing systems are of great importance for big data processing, but many challenging issues have arisen, such as the scalability of computing performance for high velocity, high variety, and high volume big data. This special session will be devoted to the most recent developments and research addressing various theoretical and practical aspects of high-performance, scalable and reliable computing. Sufficient research need to be done in these areas.

Topics of interest include:

- Parallel and Distributed Computing,
- Reconfigurable Computing,
- Big Data,
- Internet of Things,
- Soft Computing,
- Cloud Computing.

Brief bio of SS organizers:

Dr. Nibedita Adhikari: Dr Nibedita Adhikari is attached to Department of Computer Science and Engineering, Centre for Advanced Post Graduate Studies, Biju Patnaik University of Technology, Rourkela, Odisha. She has completed her MSc in Math, MCA and Mtech (CSE) from NIT, Rkl. She has completed her PhD in Computer Engineering from Sambalpur University. In her 19 years of teaching experience she has guided many research scholars in PG label and published 30 journal and conference papers. Her research area is Computer Architecture, Parallel and Distributed Processing, Soft Computing and BIG Data.

Ipseeta Nanda: Ipseeta Nanda is doing her PhD at CAPGS, Biju Patnaik University of Technology, Rourkela, Odisha, India. Her area of research is System on Chips Design and Internet of Things. She is also working as an Assistant Professor at NIIT University, Neemrana, Rajasthan, India. She has published 17 papers in peer blind reviewed Journals and in International conferences. Ipseeta Nanda completed her M.Tech from Kalinga Institute of Industrial Technology (KIIT), Bhubaneswar, Odisha, India in 2010 in Electronics and Telecommunication, with specialization in Communication System. She has 13+ years of experience in teaching in many Engineering Institutes. She has worked as a Research Assistant in the Dept. of Microelectronics & Embedded System at Asian Institute of Technology, Bangkok, Thailand in the year 2016. She has received two Best Paper Award in the field of research work one at Kuala Lumpur, Malaysia and other one at Pune, Maharashtra, India. She is acting as reviewer and speaker in many International Conferences. She is also acting as reviewer in Wiley Publication Books for Internet of Things.

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